

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Toru MORI, et al.

Serial No.: 10/085,116

Filing Date: March 1, 2002

For: STACKED CAPACITOR AND METHOD OF FORMING THE SAME
AS WELL AS SEMICONDUCTOR DEVICE USING THE SAME AND
CIRCUIT BOARD USING THE SAME



Group Art Unit: 2812

Examiner: Unknown

TC 2800 HAIL ROOM

APR 17 2002

RECEIVED

Assistant Commissioner of Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3), the relevance of these documents are discussed on pages 4-6 of the subject application. Further, concise statements of relevance and English Abstracts are attached thereto.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

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Date: April 16, 2002
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